



# MADANAPALLE INSTITUTE OF TECHNOLOGY & SCIENCE, MADANAPALLE

(UGC - AUTONOMOUS)

(Affiliated to JNTUA, Ananthapuramu & Approved by AICTE, New Delhi)

www.mits.ac.in

## Examination Section

M.Tech – I Year II Semester (R14) Supplementary End Semester Examinations February/March – 2016

Academic Year – 2015 – 16 (2014 Admitted batch only)

### TIME TABLE

TIMINGS: FN: 10:00 AM to 01:00PM

DATE / DAY	Electrical Power Systems (EPS)	Solar Power Systems (SPS)	Machine Design (MD)	Digital Electronics & Communication Systems (DECS)	VLSI & Embedded Systems (VES)	Computer Science & Engineering (CSE)
23.02.2016 Tuesday	Machine Modeling Analysis 14EPS12T07	Smart Grid Design and Analysis 14SPS12T09	Mechanical Vibrations 14MD12T07	Image & Video Processing 14DECS12T07	FPGA Architecture & Applications 14VES12T07	Java and Web Technologies 14CSP12T07
25.02.2016 Thursday	Energy Auditing, Conservation and Management 14EPS12E2a	Energy Conservation and Management 14SPS12E2b	Robotics 14MD12E2b	Wireless Sensor Communications 14DECS12E2b	Design & Testability 14VES12E2b	Cloud Computing 14CSP12E2b

**Note:** (i) Any clashes or omissions in the time table may please be informed to the Controller of Examinations Immediately.  
(ii) Even if Government declares holiday on any of the above dates, the examination shall be conducted as usual.

*S. Kumar V*

**CONTROLLER OF EXAMINATIONS**

**Date: 08.02.2016**

Copy to: The Principal

All the HODs for circulation among the staff members and display on notice board,  
The Examination Section,  
The Senior Administrative Officer,  
The Placement Officer.

*8/2/16*  
*EEE, ME, ECE, CSE*

**PRINCIPAL**

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